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Docket No.: M-9129 US

January 3, 2001

Box Patent Application Commissioner For Patents Washington, D. C. 20231

Enclosed herewith for filing is a patent application, as follows:

Inventor: Ewedemi, Odutola O.; Deng, Zhonghan J.; Motta, Ricardo Jansson; Yang, David X.D.

Title: "CMOS Sensor Array With A Memory Interface"

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X This Transmittal Letter (in duplicate) (1 page)
Page Specification (not including claims);

5 Sheets of Drawings (Figures 1, 2, 3, 4, and 5);

3 Page Declaration For Patent Application and Power of Attorney;

1 Page Recordation Form Cover Sheet (in duplicate); and

<u>3</u> Page Assignment.

Applicants assert entitlement to small entity status for the attached patent application

## CLAIMS AS FILED (fees computed under 37 CFR §1.9(f))

<u>For</u> Total Claims	Number <u>Filed</u> 21	-20	=	Number <u>Extra</u> 1	x	<u>Rate</u> \$ 9.00	=	\$ \$	Basic Fee 355.00 9.00
Independent Claims	6	-3	=	3	х	\$40	=	\$	120.00
Fee of for the first filing of one or more multiple dependent claims per application								\$	
Fee for Request for Extension of Time							<u></u>	\$	

Please make the following charges to Deposit Account 19-2386:

☐ Total fee for filing the patent application in the amount of

The Commissioner is hereby authorized to charge any additional fees which may be

required, or credit any overpayment to Deposit Account 19-2386.

**EXPRESS MAIL LABEL NO:** 

EL 487 696 177 US

Respectfully submitted,

\$

484.00

Edward C. Kwok

Attorney for Applicants

Reg. No. 33,938